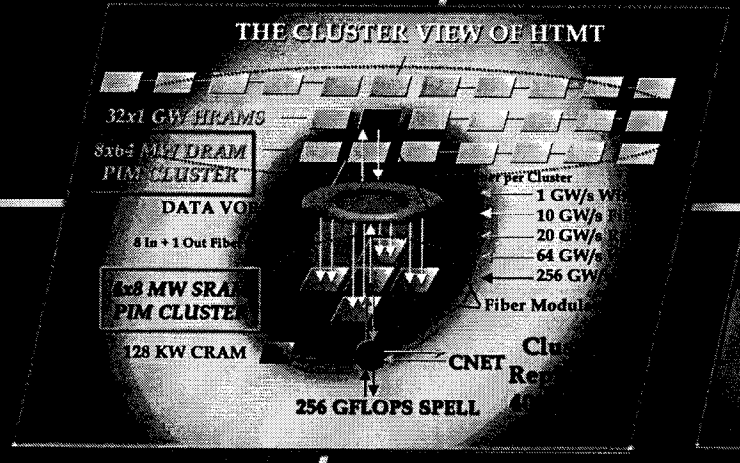
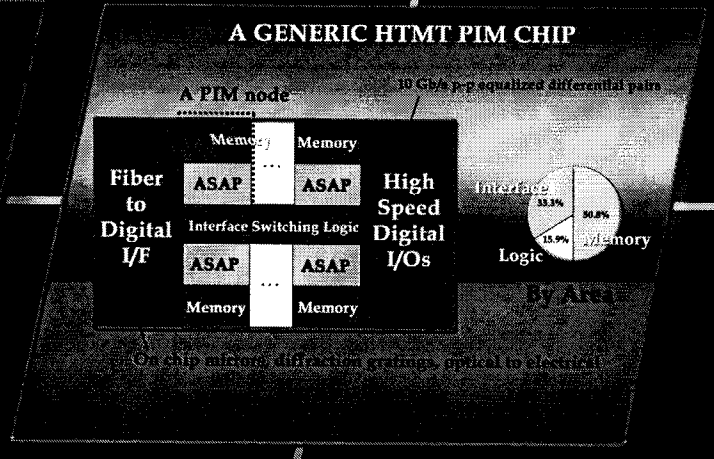
HTMT

PROCESSOR IN MEMORY

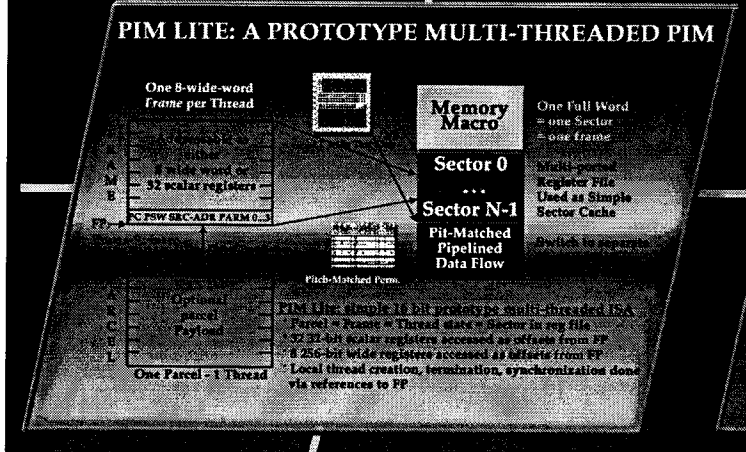
THE CLUSTER VIEW OF HTMT



A GENERIC HTMT PIM CHIP



PIM LITE: A PROTOTYPE MULTI-THREADED PIM



MAJOR REMAINING RISKS

- Basic PIM technology: becoming
- ASAP architecture: prototypes in
- Parcel model: precurser in PIM Lite
- redundancy / reconfigurability
- proof by simulation
- from HRAMs
- to HRAM
- PIM technology
- CAD tools
- design
- code from applications

BUT NO SHOW-STOPPERS

ALSO SEE THE STATE OF INDIANA BOOTH

U. of Notre Dame